WizPro200XX-X8 User Manual V1.0



WizPro200XX-X8 User Manual

1. Supported chips:

Support continuous upgrading and improvement of brands and models; Supports serial number functionality. The serial number is 4 bytes long and stored in Flash.

The address can be freely set by the user through a PC application, and the initial value and cumulative amount of the serial number can also be set by the user at will.

- WizPro200XX-X8 refers to the one to eight dedicated programmer of XX model ;
- WizPro200RS-X8: Renesas 32-bitSupperH series, 32-bitRX series, H8Sxx series ;
- MPlus300RX-X8: Renesas RH850xx, RA series;

• WizPro200ST8-X8: ST STM8xx、STM32F0、STM32F1、STM32F2、STM32F3、STM32F4、STM32L0、STM32L1、STM32L4、STM32L4+、STM32G0、STM32G4、STM32L5、STM32W1、STM32C0 series;

WizPro200SHWB-X8: STM32H7、STM32WL、STM32WB、H5、U5 series ;

• WizPro200CY-X8: Cypress PSoC1, PSoC3, PSoC4, PSoC5, PSoC6, USBTypeC series, TrueTouch, Multi-Touch series ;

• WizPro200PIC-X8: Microchip PIC10xx, PIC12xx, PIC16xx, PIC18xx, PIC24xx, MCP19xx, dsPIC3xx series ;

- WizPro200AT-X8: Atmel ATTinyxx, ATMegaxx, AVR, XMEGA, SAM series ;
- WizPro200EFM-X8: SiliconLabs EFM8xx、EFM32xx、EFR32xx、EZR32xx、EFRModules series ;
- WizPro200SLB-X8: SiliconLabs C805xx, Si4010xx, Sil0xx, CPTxx series ;
- WizPro200EPS-X8: Qorvo, Epson, Ambiq , PAC5xx, S1C31xx, APOLLOxx, AMA3xx series;
- WizPro200MSP-X8: TI MSP430F1xx/2xx/4xx/5xxx series, DRV91670、DRV91680 ;
- WizPro200NFP-X8: NXP PCF79xx, KinetisKxx, LPC, S9KExx, MC9xx, FS32xx series ;

• WizPro200WL-X8: Nodic、Memsic、ITON、TI,CC25xx,CC24xx,NRF518x2、NRF528x2、NRF24LExx、NRF24LU1xx、NRF31562、MXD2660,MXD27xx series;

• WizPro200NX-X8: NEC All Flash series 8/16/32-bits MCU、R8Cxx and M16C series 、RL78 series, and Renesas other series MCU;

- WizPro200LAP-X8: LAPIS ML63Q466 ;
- WizPro200INF-X8: Infineon S6E1xx, S6E2xx, CY9xx series ;
- WizPro200MLX-X8: Melexis MLX81113KDC
- WizPro200AUR-X8: Aurasemi Au53xx, Au56xx
- WizPro200BYD-X8:BYD BF7xx, BS9000xx series ;
- WizProXF-Plus-X8: SPI Flash, MicroWave 93Cxx, SPI and I2C EEPROMs;



2. Appearance dimensions and interface diagram: (length, width, height: 275mm * 155mm * 49mm)







3. Indicator lights:

3.1. Power indicator light: After the programmer is powered on, the indicator light lights up, indicating that the power is normal;

3.2. Burning result status indicator lights (red and blue LED lights):

3.2.1 When the programmer downloads the program and connects it to the power supply:

•Red and blue indicator lights flashing alternately: indicates that the system is undergoing internal data verification;

•The internal data verification of the system has failed, and the red light remains on: it is necessary to connect to the computer and download the program again in order to burn and write normally;

• The internal data verification of the system is successful, and the blue light remains on for a long time; Can start burning chips;

3.2.2 When the programmer starts programming after completing the verification: (start button or provide trigger signal)

•The blue and red indicator lights flash alternately, indicating that the programmer is programming the target chip;

•After programming for a few seconds, the red light will light up: indicating that the programmer for the target chip has failed. Please perform the corresponding check;



•After programming for a few seconds, the blue light turns on: indicating successful programming of the target chip;

4. Key and interface instructions:

4.1. White button: Programmer button, press the button once to start programming all target chips (n channels);

4.2. Power interface: Connect a 9-12V DC adapter, 4A is sufficient, and it is equipped with a random DC power adapter;

4.3. USB interface: used for downloading programs or online programming, as well as updating and setting internal data of the programmer;

4.4. Programming Interface: Used for programming MCU. The end pointed by the arrow in the ribbon cable is the first pin. Pay attention to the insertion direction of the ribbon cable (with anti error design)

5. Expansion interface description:

5. Extension interface description:

5.1 Signal definition and description: Key input low level effective (>100ms), OK/NG: high level effective, burning write OK/NG

Output is low.

- NGx is the x-th NG state output (high level)
- OKx is the x-th OK state output (high level)
- BUZYx is the x-th BUSY signal
- KEYx is the x-th independent programming button input
- Programmer key input (pulse>100ms) low level effective)





5.1 Left 20PIN interface:

| 信号 <mark>说</mark> 明 | KEY7 | NG7 | KEY8 | NG8 | GND | BUSY5 | OK5 | BUSY6 | OK6 | 5V |
|---------------------|-------|-----|-------|-----|------|-------|------|-------|-----|-----|
| 引脚 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 17 | 19 |
| 引脚 | 2 | 4 | 6 | 8 | 10 | 12 | 14 | 16 | 18 | 20 |
| 信号说明 | BUSY7 | OK7 | BUSY8 | OK8 | KEY5 | NG5 | KEY6 | NG6 | 3V3 | GND |

5.2 Right 20PIN interface::

| 信号说明 | KEY7 | NG7 | KEY8 | NG8 | GND | BUSY5 | OK5 | BUSY6 | OK6 | 5V |
|------|-------|-----|-------|-----|------|-------|------|-------|-----|-----|
| 引脚 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 17 | 19 |
| 引脚 | 2 | 4 | 6 | 8 | 10 | 12 | 14 | 16 | 18 | 20 |
| 信号说明 | BUSY7 | OK7 | BUSY8 | OK8 | KEY5 | NG5 | KEY6 | NG6 | 3V3 | GND |

6 All WizPro200XX-X8 series programmer has the similar PC interface, here use WizPro200Nx-X8 as the example for description.

6.1 Starting "WizPro200Nx.exe" PC application:

A window as above is pop-up, select the correct COM port (Normal, when USB driver is installed, select the one with maximum digital's COM port), and then click Yes to enter to Main form;

6.2 Main Form:





6.3 Button Description:

Basic button function:



6.4 Online programming button:

a) Click to do automatic execute programming functions, it will check thechip blank first, if its blank, do programming directly, otherwise, do erasing chip functions first and then do programming, after programming, verification will be executed and then write the security setting;

Click to execute the chip erasing function;

🛛 💸 <u>B</u>lank Check

b)

e)

Click to do chip blank check;

Y) * Program

Click to programming the data to chip flash memory;

Click to do the Checksum function and compare with current loaded data checksum, if it is same, then write the security setting to the chip, otherwise, report the error and then stop operation;



6.5 Function Description:

6.5.1 Select the MCU chip type:

- See below diagram, click the "MCU Type" page, and then click the "『 button, a select menu will popup, click the items according to your chip, here, "uPD78F9234" under the "78KOS/Kx1+" series is selected;
- After select the chip, If hexadecimal file has loaded, the checksum will be re-calculated according to the selected chip's flash memory size.

| 00 VizPro2 | OON | e Pr | ogra | mile | e Vi | zaro | 1 - 1 | For | NEC | <u>A11</u> | Fla | ish | | | | |
|-----------------------------|--------------|---------|---------|--------------|----------|-------|-------|------|--------------|-----------------|------------------|--------------|---------------|--------------------|---------------------|---------------------------------------|
| <u>F</u> ile <u>E</u> xecut | е <u>Н</u> е | elp | | | | | | | | | | | | | | 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |
| 😮 E <u>x</u> it | B L | oad | | <u>S</u> ave | 2 | ן פ | ownLo | oad | | 13-] AI | uto | 3 | <u>B</u> lank | < Check | 🍫 <u>P</u> rogram 🚽 | [™] ⊻erify |
| MCUTupo | | | പല | Dura | | L bi | | | | 78 | 3KOS/I | Kx1+ | • | DNx Firmware V1.0, | MaxWiz Technol | ogy |
| мсо туре | Pro | ogran | n VF | Pro | ectio | n N | ump | erco | ntr | 78 | 3KO/KI | B1+ | + | uPD78F0500 (A) | limited * | ~ |
| MCLI Type: | D | 070 | 5051 | 2/41 | 2 | | | - 1 | - | 78 | 3KO/KU | C1+ | • | uPD78F0501 (A) | ********* | |
| тоо туре. | lac | 070 | 1 0 3 1 | J(A) | | | | | J J J | 78 | SKO/KI | 01+ 71+ | | uPD78F0502 (A) | FFF. | |
| | . 10 | | | | | | | | EC | 78 | SKU/KI | 21+ 71+ | | uru (8F0503 (A) | | |
| BIOCK Size | . 11 | JZ4 D | ytes. | FIB | sn Si | ze: | | | 7/5/ | | | inerine . | 10 | ar brorososb (k) | - | |
| Block Nurr | ib: 32 | 2 Bloc | cks | 32 | 2K By | rtes. | (| C Re | enesa | 78 | 3KO/I/ | 42 | | uPD78F0511 (A) | | ~ |
| Object File: | <u>h</u> . | \Bina | mFile | -\He | 8k h | ev | | | _ | 78 | 3KU/11 3KO/11 | 52 12 | | uPD78F0512(A) | | |
| Program Flash | 1 | .D IIIC | | | JOK. III | un | | | | |)KO/ I . | 3.0 | | uPD78F0513D (A) | | - |
| | | | | 1.2 | | E | | 7 | 0 | 78 | 3KO/F: | ₂ | • | uPD78F0514 (A) | | |
| 0.00000000 | 0 | 1 | 2 | 3 | 4 | 5 | Б | 1 | 8 | 78 | 3KO/K | : 2 | • | uPD78F0515 (A) | 12 | |
| 0x00000000 | 82 | 00 | FF CC | FF FF | FF | FF | FF | FF | FF FF | 78 | 3KO/K3 | 2-L | • | uPD78F515D (A) | | |
| | FF | FF. | FF. | FF | FF | FF | FF | FF | FF | 78 | 3KO/L | 2 | • | 11PD78F0521 (A) | - | _ |
| 0x00000020 | FF | FF. | FF. | FF. | FF. | FF | FF | FF. | FF | 78 | 3KO/L | κ3 | | uPD78F0522 (A) | | _ |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | 78 | KOR/I | Ex3 | • | uPD78F0523 (A) | | _ |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 78 | 3KOR/1 | Kx3 | + | uPD78F0524 (A) |].`.d.g.j.n. | _ |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 78 | SKOR/I | Kx3-L | | uPD78F0525 (A) | ł | _ |
| 0x00000060 | 9C | 01 | A7 | 01 | FF | FF | FF | FF | FF | 78 | 3KOR/1 | LX3 | • | uPD78F0526 (A) | (17.151) | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | uF | D79F9 | 9211 | | uPD78F0527 (A) | | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 10 | 22 | 5C | υF | 2078F(| 0711 | - | WID10F0521D(A) | _ '\C.t. | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | uł | D78F | 0712 | | uPD78F0531 (A) | a.edc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | ED | ~ ~ | FE | -CC | uPD78F0532 (A) | xv. | |
| 0x000000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | uPD78F0533(A) | | |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | uPD78F0535(A) | < | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 30 | uPD78F0536 (A) | | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | uPD78F0537 (A) | | |
| 0x000000F0 | EF | 8C | 30 | F3 | 22 | B2 | 0A | FO | 00 | 00 | 22 | AB | 01 | uPD78F0537D (A) | "0 | ~ |



6.5.2 Select the Programming Interface:

- For different MCU series, they may have one, two or three programming interface, UART, CSI and CSI+HS, the system can filter the selection according to chip selection, such as, for uPD78F9234, only UART interface is available, so, it is not necessary to select the programming interface.
- For On-Board or in-circuit programming, according to the customer power system, it may use 3.0V or 5.0V to design the whole system, so in order to compatible the whole board system, 3.3V or 5.0V signal could be used; If use socket to program chip independently, both 3.3v and 5.0V are OK;
- Normally, we recommend to use Programmer to provide the oscillation clock for the chip, the programmer can optimize the clock frequency to fit the chip's requirement and to improve speed of programming, if On chip oscillator or on-board crystal is used as the chip's source clock, Unselect the "Use Ext. Clock(8/16Mhz)" option;

| 🗱 VizPro2 | 20016 | s Pr | ogra | nne | r Vi | zar | 1 - 1 | For | NEC | A11 | Fla | ısh | | | | | | |
|-----------------------------|--------------|-------|---------|--------------|--------|---------|--------|---------|--------|-------------------|----------------|----------------|------------------|----------------|---------------|--------------|--------------------------|--------|
| <u>F</u> ile <u>E</u> xecut | e <u>H</u> e | elp | | | | | | | | | | | | | | | | |
| 🕐 E <u>x</u> it | | oad | | <u>S</u> ave | 6 | 1 🖸 | ownL | oad | | <mark>⊳!</mark> ∆ | uto | <u> 1</u> | <u>B</u> lank I | Check | - | <u>E</u> ra | se 🦠 <u>P</u> rogram 🖌 🖞 | /erify |
| | Pro | noran | n I/E | Pro | tactio | n I N | umb | or Co | ntre 🗲 | | | ₩izPi | ro200 | Nx F | irmwa | ire V | I.O, MaxWiz Technolog | y |
| мсо туре | | gran | | FIU | eciio | - CL | umb) | | unut_ | | * | No | pro; | gramm | able | Numbe | er is limited * | ~ |
| Interface Mo | de | | -I/F | Volta | ge — | 6 | Outo | ut Clo | ck | | > MC | U Fla | ish S | tart | Addre | ss = | 0x000000. | |
| U UART MO | ode | | | 3.3V | 6 | C | Inter | nal Clo | ock | | > ML > Fi | le St | ash Ei tart i | nd Ad Addre | dress ss = | = 0x000 | 0x007FFF. | - |
| CSI Mode | 9 | | C | 5V | | C | On-B | loard (| JSC | | > Fi > Fi | le Er le Ch | nd Ad neck S | dress Sum = | = 0x4F | 0x001 ED. | .FFF. | |
| C CSI+HS N | Vlode | | | | S | elect (| Clock: | 8 | MHz | - | > MC | U Che | eck S | Um = | OxAF | ED. | | |
| | _ | | | | - | | | | | _ | J | | | | | | | × |
| Object File: | D: | \Bina | aryFile | e\He | (8k.h | ex | | | | | | | | | | | | |
| Program Flash | | | | | | | | | | | | | | | | | | |
| | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | В | С | D | E | F | Strings | ^ |
| 0x00000000 | 82 | 00 | FF | FF | FF | FF | FF | FF | FF | FF | E4 | 02 | FF | FF | FF | FF | | |
| 0x00000010 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | DD | 02 | FF | FF | FF | FF | | |
| 0x00000020 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 01 | 67 | 01 | 6A | 01 | 6E | 01 | @.O.].`.d.g.j.n. | |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 01 | 85 | 01 | 8B | 01 | 91 | 01 | r.v.z.} | |
| 0x00000060 | 9C | 01 | A7 | 01 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | 1 | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 1C | 22 | 5C | 01 | 0A | 43 | E9 | 74 | FE | E9 | ''\C.t | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | 65 | FE | E9 | 64 | FE | E9 | 63 | u.,`aedc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | E9 | 77 | FE | CO | E9 | 76 | FE | bxwv. | |
| 0x000000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | EB | 8C | 88 | | |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | 00 | EF | 8C | 0 | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 3C | 06 | 2F | EB | 0 | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | 0A | F3 | 00 | 0< | |
| 0x000000F0 | EF | 8C | 30 | F3 | 22 | B2 | QA | FO | 00 | 00 | 22 | AB | 01 | 30 | FE | A6 | 0.""0 | ~ |

MaxWiz Technology Co.,Ltd.

6.5.3 Protection and security setting:

- Click "Protection" to enter this setup page;
- This option use to Chip's security set.

| 🗱 VizProž | 200N | x Pr | ogra | anne | r Vi | zaro | 1 - 1 | For | NEC | <u>A11</u> | Fla | ish | | | | | | |
|----------------------------|-------------|---------|--------|--------------|--------|--------|-------|-------|--------|------------|---------------|----------------|-----------------|----------------|---------------|--------------|----------------------|----------|
| <u>F</u> ile <u>E</u> xecu | te <u>H</u> | elp | | | | | | | | | | | | | | | | |
| 🔇 E <u>x</u> it | | oad | | <u>S</u> ave | 1 | 1 0 | ownL | oad | | ₽lA | uto | % 1 | <u>3</u> lank | Check | - | Era | se 🦠 <u>P</u> rogram | ✓ Verify |
| | Pr | ograr | n I/E | Pro | tectic | in I N | lumb | er Co | ntra 🕻 | | | ₩izP | ro200 | INx F | irmwa | ire V | 1.0, MaxWiz Techn | ology |
| тисо туре | 1 EU | Jyrai | n yn | 110 | | [13 | umb | erco | nut_ | | * | N | o pro | gramm | able | Numbe | er is limited * | |
| Chip Secur | ity | | 10 | | | Ī | | | | | > MC | U Fl | ash S | tart | Addre | ss = | 0x000000. | |
| I∕ Fort | oit Bloo | ck era: | se! | | | | | | | | Fi | le St | ash E | na Aa Addre | aress ss = | 0x000 | 0x00/FFF. | - |
| Fort | bit Bloo | ok era: | se! | | | | | | | | > Fi > Fi | le En le Cl | nd Ad neck (| dress Sum = | = 0x4F | UxUUI ED. | LFFF. | |
| Fort | oit Chip | o erase | el. | | | | | | | | > MC | U Ch | eck S | Um = | OxAF | ED. | | |
| | - | | | | | | | | | | ļ | | | | | | | × |
| Object File: | D | \Bina | aryFil | e\He | k8k.h | ex | | | | | | | | | | | | |
| Program Flash | | | | | | | | | | | | | | | | | | |
| 1 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | В | С | D | E | F | Strings | <u>^</u> |
| 0x00000000 | 82 | 00 | FF | FF | FF | FF | FF | FF | FF | FF | E4 | 02 | FF | FF | FF | FF | | |
| 0x00000010 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | DD | 02 | FF | FF | FF | FF | | |
| 0x00000020 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | - 9 |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 01 | 67 | 01 | 6A | 01 | 6E | 01 | @.O.].`.d.g.j.n. | |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 01 | 85 | 01 | 8B | 01 | 91 | 01 | r.v.z.} | |
| 0x00000060 | 90 | 01 | A7 | 01 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 1C | 22 | 5C | 01 | 0A | 43 | E9 | 74 | FE | E9 | ''\C.t. | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | 65 | FE | E9 | 64 | FE | E9 | 63 | u`aedc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | E9 | 77 | FE | CO | E9 | 76 | FE | Ьхwv. | |
| 0x000000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | EB | 8C | 88 | | - |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | 00 | EF | 8C | 0 | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 3C | 06 | 2F | EB | 0 | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | 0A | F3 | 00 | 0< | - |
| 0x000000F0 | EF | 8C | 30 | F3 | 22 | B2 | 0A | FO | 00 | 00 | 22 | AB | 01 | 30 | FE | A6 | 0.""0 | \sim |

MaxWiz, Technology Co., Ltd.

6.5.4 Programming Number control:

- Click "Number Control" to enter this setup page;
- If programmable number control is used for off-line programming, check the option of "En able Number Control", then the required number could be set by user;

| 🗱 VizPro2 | OON | s Pr | ogra | anne | r Vi | zaro | 1 - | For | NEC | A11 | Fla | ish | | | | | | |
|-----------------------------|------------|---------|---------|--------------|-------|------------|--------|-------------|------|------------------|-----------------|----------------|------------------|----------------|----------------|---------------|--|--------|
| <u>F</u> ile <u>E</u> xecut | e <u>H</u> | elp | | | | | | | | | | | | | | | | |
| 🔇 E <u>x</u> it | | ,oad | | <u>S</u> ave | 3 |) D | ownL | oad | | r >! A | uto | <u> 1</u> | <u>B</u> lank (| Check | - | <u>E</u> ra | se 🦠 <u>P</u> rogram 😽 | Verify |
| D | -1.5 | | | Num | borf | `ontro | a lo | NI O | | | | WizPi | ro200 | Nx Fi | irmwa | re V1 | .0, MaxWiz Techno | logy |
| Program I/r | - P | rotect | ion | Num | berc | -onur | , 15 | | ontr | | * | No | pro | gr amm | able | Numbe | er is limited * | ~ |
| Number | | | | 11 | Info | | | | | | | U Fla | ish St | tart. | ***** Addre | ***** 55 = | ************************************** | |
| T Enable | Num | ber Co | ntrol | | Pre | set Nu | imber: | | | | > MC > Fi | U Fla le St | art A | ad Ad Addre | dress ss = | = 0x000 | 0x007FFF. 1000. | |
| | _ | esesese | 0.0.00 | | Use | ed Nur | nber: | 555 | | | > Fi > Fi | le Er le Ch | id Add leck S | bress Sum = | = 0x4F | 0x001 ED. | FFF. | |
| Total Numbe | er: 0 | | \$ | | Res | st Num | ber: | 2222 | | | > MC | U Che | ck Sl | Jm = | OxAF | ED. | | |
| | | | | SI. | | | | | | - | | | | | | | | × |
| Object File: | D: | \Bina | aryFile | e\He | (8k.h | ex | | | | | | | | | | | | |
| Program Flash | | | | | | | | | | | | | | | | | | |
| | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | В | С | D | E | F | Strings | |
| 0x00000000 | 82 | 00 | FF | FF | FF | FF | FF | FF | FF | FF | E4 | 02 | FF | FF | FF | FF | | |
| 0x00000010 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | DD | 02 | FF | FF | FF | FF | | |
| 0x00000020 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 01 | 67 | 01 | 6A | 01 | 6E | 01 | @.O.].`.d.g.j.n. | |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 01 | 85 | 01 | 8B | 01 | 91 | 01 | r.v.z.} | |
| 0x00000060 | 9C | 01 | A7 | 01 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 1C | 22 | 5C | 01 | 0A | 43 | E9 | 74 | FE | E9 | ''\C.t. | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | 65 | FE | E9 | 64 | FE | E9 | 63 | u`aedc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | E9 | 77 | FE | CO | E9 | 76 | FE | bxwv. | |
| 0x00000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | EB | 8C | 88 | | |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | 00 | EF | 8C | 0 | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 3C | 06 | 2F | EB | 0 | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | 0A | F3 | 00 | 0< | |
| 0x000000F0 | EF | 8C | 30 | F3 | 22 | B2 | 0A | FO | 00 | 00 | 22 | AB | 01 | 30 | FE | A6 | 0.""0 | ~ |

6.5.5 Serial No. function:

- Click the "S/N Control" to enter this setup page;
- If user want to use serial No. function, enable the "Enable S/N" option first, then user can set the starting serial No. value (4-Bytes), gain value of serial No. adjustment and the location in chip's flash memory;
- This function is very useful for some product, such as remote controller product, normally, each remote controller has a unique ID address, use this function, software engineer can map this ID address to a fixed flash memory address, and during mass product, use this function to burn the ID address;



This option just affect the off-line programming unless "Enable On-Line S/N" is selected;

| 🗱 VizPro2 | OON | e Pr | ogra | | e Vi | zar | 1 - 1 | For | NEC | <u>A11</u> | Fla | ish | | | | | | |
|-----------------------------|--------------|-------|--------|--------------|-------|------------|---------|---------|-----|------------|------------------------------|-------------------------|-----------------|-------------------------|------------------------|-------------------------------|--------------------------------------|----------|
| <u>F</u> ile <u>E</u> xecut | e <u>H</u> e | elp | | | | | | | | | | | | | | | | |
| 🔇 E <u>x</u> it | | oad | | <u>S</u> ave | |) D | ownLo | bad | | r⇒]∆ | uto | 3 | <u>B</u> lank | Check | - | . <u>E</u> ra | se 🦘 <u>P</u> rogram 🔹 | ✓ Verify |
| Number Co | introl | S/N | l Cor | itrol | Com | imuni | catio | n | | | * | WizP | o pro | IN x F | irmwa able ***** | ire V Numbe **** | 1.0, MaxWiz Techno r is limited * | ology |
| Initial S/N: | 0x. | 3D00 | 01F8 | | | | | Enable | s/N | | | U Fla | ash S | tart | Addre | = 22 | 0x000000. | |
| Gain of S/N: | 0×0 | 01 | | | | F Er | nable (|)n-line | S/N | | > Fi > Fi > Fi > Fi | le St le En le Ch | tart d nd Ad | Addre dress Sum = | ss = = 0x4F | 0x000 0x001 ED. | 0000. FFF. | |
| S/N Address: | 0×0 | 00008 | 88 | | | Hexde | ecimal | format | | | > MC | U Che | eck S | Jm = | 0xAF | ED. | | ~ |
| Object File: | D: | \Bina | rvFile | e\He | .8k.h | ex | | | | | , | | | | | | | |
| Program Flash | 1 | | | | | | | | | | | | | | | | | |
| | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | В | С | D | E | F | Strings | ^ |
| 0x00000000 | 82 | 00 | FF | FF | FF | FF | FF | FF | FF | FF | E4 | 02 | FF | FF | FF | FF | to a decision and the | |
| 0x00000010 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | DD | 02 | FF | FF | FF | FF | | |
| 0x00000020 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 01 | 67 | 01 | 64 | 01 | 6E | 01 | @.0.].`.d.g.j.n. | |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 01 | 85 | 01 | 8B | 01 | 91 | 01 | r.v.z.} | |
| 0x00000060 | 9C | 01 | A7 | 01 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 1C | 22 | 5C | 01 | 0A | 43 | E9 | 74 | FE | E9 | '"\C.t. | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | 65 | FE | E9 | 64 | FE | E9 | 63 | u.`aedc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | E9 | 77 | FE | CO | E9 | 76 | FE | bxwv. | |
| 0x000000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | EB | 8C | 88 | | |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | 00 | EF | 8C | 0 | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 3C | 06 | 2F | EB | 0< | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | 0A | F3 | 00 | 0< | |
| 0x000000F0 | EF | 8C | 30 | F3 | 22 | B2 | 0A | FO | 00 | 00 | 22 | AB | 01 | 30 | FE | A6 | 0.""0 | ~ |

6.5.6 Communication configuration:

- ullet Click the "Communication" to enter this setup page;
- If this application start-up before connect the device, user must to enter this page first;
- Click "FreshList" button to update the COM port list; then select the correct the COM port according to the Programmer;
- Click "**Find Device**" to establish the connection between application and Programmer;
- "Auto detect Chip connection" use to turn on the Auto-detection function to check if the chip is connected, the time setting is use to set the stable time of Chip in or chip out.

| VizPro2 | OON | s Pr | ogra | anne | r Vi | zar | 1 - | For | NEC | A11 | F1: | ash | | | | | | |
|--------------------|-------------|--------|---------|--------------|-------|----------|--------|--------|-------|-------------------|---------------|----------------|------------------|----------------|-----------------|--------------|------------------------|--------|
| lle <u>E</u> xecut | .е <u>Н</u> | elp | | | | | | | | | | | | | | | | |
| 😮 E <u>x</u> it | | oad | | <u>S</u> ave | 1 | 1 0 | ownL | oad | | <mark>⊳]</mark> ∆ | uto | 3 (| <u>B</u> lank (| Check | - | <u>E</u> ra: | se 🦠 <u>P</u> rogram 🖌 | Verify |
| Number Co | introl | | J Cor | otrol | Corr | nmun | icatio | n l | | | 1 | WizPi | ro200 | Nx F | irmwa | re V1 | I.O, MaxWiz Technol | ogy |
| rumber oc | muor | 100 | 4 001 | luor | 00 | | | 1 | - | | * | No | pro: | gr amm | able | Numbe | r is limited * | ^ |
| Select Devic | e: | A900 | 19cxh | | | - | | Fres | h Lis | t | > MC | U Fla | ash St | art | Addre | ss = | 0x000000. | |
| E Auto dete | ect Ch | io con | nectio | n | | | | | | | Fi | le St | ish Er tart J | ia Ad iddre | aress ss = I | 0x000 | 0000. | - |
| Chip In stable | time | (ms): | | 600 | - | • | F | Find [| Devid | e | > Fi > Fi | le Er le Ch | nd Add neck S | iress Sum = | = 0x4F | UxOO1 ED. | FFF. | |
| Chin out stab | le time | e(ms): | | 600 | 2 | | | | | | > MC | U Che | eck Sl | Jm = | 0xAF | ED. | | |
| Ship Out stud | a surre | duro). | | 000 | | - | | | | | ļ | | | | | | | ~ |
| bject File: | D: | \Bina | aryFile | e\He | k8k.h | ex | | | | | | | | | | | | |
| Program Flash | | | | | | | | | | | | | | | | | | |
| | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C | D | E | F | Strings | ^ |
| 0x00000000 | 82 | 00 | FF | FF | FF | FF | FF | FF | FF | FF | E4 | 02 | FF | FF | FF | FF | | |
| 0x00000010 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | DD | 02 | FF | FF | FF | FF | | |
| 0x00000020 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000030 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000040 | 40 | 01 | 4F | 01 | 5D | 01 | 60 | 01 | 64 | 01 | 67 | 01 | 6A | 01 | 6E | 01 | @.0.].`.d.g.j.n. | |
| 0x00000050 | 72 | 01 | 76 | 01 | 7A | 01 | 7D | 01 | 80 | 01 | 85 | 01 | 8B | 01 | 91 | 01 | r.v.z.} | |
| 0x00000060 | 9C | 01 | A7 | 01 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000070 | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | FF | | |
| 0x00000080 | F5 | 87 | FO | F6 | FE | E6 | 1C | 22 | 5C | 01 | 0A | 43 | E9 | 74 | FE | E9 | ''\C.t. | |
| 0x00000090 | 75 | FE | E9 | 60 | FE | E9 | 61 | FE | E9 | 65 | FE | E9 | 64 | FE | E9 | 63 | u`aedc | |
| 0x000000A0 | FE | 0A | C3 | E9 | 62 | FE | FO | 78 | FE | E9 | 77 | FE | CO | E9 | 76 | FE | bxwv. | |
| 0x000000B0 | FC | D5 | 01 | F8 | 00 | FE | DC | E2 | F1 | 01 | 3C | 06 | 2F | EB | 8C | 88 | | |
| 0x000000C0 | 30 | F4 | FC | 98 | FE | DC | E2 | BB | FE | 3C | 07 | 0A | F3 | 00 | EF | 8C | 0 | |
| 0x000000D0 | 30 | F3 | FC | 82 | 00 | F8 | 20 | FE | DC | E2 | 82 | 00 | 3C | 06 | 2F | EB | 0 | |
| 0x000000E0 | 8C | 88 | 30 | F4 | FC | 20 | FE | DC | E2 | 20 | FE | 3C | 07 | 0A. | F3 | 00 | 0< | |
| 0x000000F0 | EF | 80 | 30 | F3 | 22 | B2 | 0A | FO | 00 | 00 | 22 | AB | 01 | 30 | FE | A6 | 0.""0 | ~ |

6.5.7 Load Hexadecimal File:

• After select the Chip, click application;

button to load the object binary file to

Hexadecimal format and binary format are supported;

🖹 Load "

"

| VizPro | 2001 | Nx P | rogr | amme | er V | izar | d – | For | NEC | A 1 | 1 F1 | ash | | | | | | |
|-------------------|------|---------|------------|--------------------|--------------------|-----------------|-------------------|--------|--------|--------------|--------|-------|---------------|------------------|------------------|-------------|-----------------------|----------|
| ile <u>E</u> xecu | te j | Help | | | | | | | | | | | | | | | | |
| 🗶 E <u>x</u> it | ₿. | Load | | <u>S</u> ave | 8 | 1 D | ownloa | əd | r | ≽ ! ∆ | uto | 2 | <u>B</u> lank | Checł | - ¹ 3 | <u>E</u> ra | ase 🦠 <u>P</u> rogram | ✓ Verify |
| | 1 | | | | | | | | | | 1 | | | | Devi | ce = | ?????? | |
| ИCU Туре | e P | rog. l/ | /F P | rotec | tion | S/N | | Conf | ig | | F | lash | Check | sum = | OxE2 | 23. | | ~ |
| tarting S/N: | 1 | 打开 | 8 | | | | | | | | | | | | | | ? | |
| ain control: | | 查 | 找范围 | 3(I): | 6 | Bina | ryFil | e | | | | | + | 1 🔹 | - E | r | | |
| | | | - | | | 7/11 | hav | | | | | Batro | עדע | 4 | 10.100 | | | |
| tarting Addre | SS: | | 3 | | | CarSys | stem.] | hex | | | Sony | BatS. | HEX | | | | | |
| | _ | 我最 | 近的 | 文档 | | CarSy | sWire. | hex | | 1 | UPRG | EPP. | Hex | | | | | |
| bject File: | | | 2 | <u> </u> | | 232V: | 11. hez | ĸ | | - | UPRG | -NEC- | ExtAd | ldr. H | X | | | |
| | 0 | | 百面 | | | lex1k. | hex | | | 1 | UPRG | -NEC- | Lines | er.HEX | { ?¥ | | | |
| «00000000 | 5E | | <u>未</u> 回 | | | lex2k. fev4k | hex hey | | | 44 | I UPRG | -NEC- | motro | la. Ju lard l | 1X TEX | | | |
| «00000010 | 38 | | | | | lex8k. | hex | | | | | 1.20 | 1.0110 | | | | | |
| (00000020 | 34 | 我 | 的文 | 挡 | | EC05 | 37. hez | ¢ | | | | | | | | | | |
| <00000030 | 38 | | | | | TEC-12 | 28K. h | ex | | | | | | | | | | |
| (00000040 | D | 我 | 前曲 | सित्ते । सित्ते | | PIC128 | SK.hez | к / | | | | | | | | | | |
| (00000050 | ΕĒ | - | | | | SonyB | at. 16. atE32. | HEX | | | | | | | | | | |
| (00000060 | 8E | | - | 5 | | SonyB | atE321 | Y. HEX | | | | | | | | | | |
| 00000070 | 28 | P |]上邻 | 居 | 1 | SonyB | atE. H | EX | | | | | | | | | | |
| 00000080 | 6E | | | | | | | | | | | | | | | | | |
| (00000090 | 93 | | | | - 1+ /4 | - 17 (M | | v | | | | | | | | - | tTII (0) | |
| 000000A0 | 49 | | | | 219 | |). | Ine | xor. n | ex | | | | | | - | 1177 (1) | |
| 00000080 | A5 | | | | 文件 | 类型 | (I): | A | l Hex | Fil | .es (* | Hex) | | | | • | 取消 | |
| 000000000 | D2 | 06 | 65 | 67 | 25 | 07 | 48 | 06 | 57 | 56 | AB | UC | 5A | 05 | 6E | 35 | eg%.K.WVZ.n5 | |
| :000000D0 | 69 | OB | 52 | BF | 52 | OB | 25 | OB | OB | 6D | 4B | 0A | AB | 04 | BB | 52 | i.R.R.%mKB | |
| 000000E0 | AD | 05 | 6A | OB | AA | 2D | 92 | 0D | A5 | 7E | 25 | 0D | 55 | 0A | 4D | 5A | j~%.U.MZ | |
| (000000F0 | B6 | 04 | B5 | 05 | 00 | 29 | 00 | 00 | 29 | 00 | 29 | 00 | 00 | 29 | 00 | 00 |)).)) | |
| .00000100 | 20 | 00 | 29 | 00 | 00 | 30 | 00 | 00 | 29 | 00 | 00 | 29 | 00 | 29 | 00 | 00 | ມດາມ | ~ |

6.5.8 Save button : click this button will convert the loaded data to binary file format for other purpose;

6.5.9 Download Function :

- Click this button to download all configuration data and object file to programmer for off-line programming, before click this button, chip type must be selected and hexadecimal file must be loaded; otherwise, error message will pop-up to remind user;
- All setting and data will be stored permanently, unless new data will be downloaded;
- If Serial number function is used, download command will reset serial No. counter and set with new serial No. setting (disable or new serial No.);
- If Programmable number control function is enabled, download command will clear all programming counter and set with new setting (new Number or disable it);



7 Programming:

Off-Line Programming:

- 7.1 First, connect programmer to PC with USB cable, select the chip and load your object Hex file and download to the programmer. Then disconnect the USB cable and re-power on the programmer ;
- 7.2 Connect the cable to your target chip by socket or target board;
- 7.3 After the above steps, press the programming key to start the programming. If programming successful, one beep will be heard and blue LED will turn on, otherwise, red led will turn on. If programming failure, please check the connection and try again;
- 7.4 Replace another chip and do 7.3 step again;
- 7.5 If Programming quantities function is enabled, The programmer will not response to the key press when the preset quantity is arrived and the blue LED and red LED will blink alternatively.
- 7.6 If serial No. function is enabled, the programmer will write the serial No. data to the specified chip memory address, and after programming one chip successfully, the serial No. will automatically increase (set by PC).

On-Line Programming with PC:

- 8.1 Connect the Programmer in USB to PC.
- 8.2 Start the PC Application, then select the chip name, upload the object binary file (default is Hex format).
- 8.3 Setup the option Byte according to your design.
- 8.4 Click on-line button



8 Package List:

- 8.1 WizPro200XX-X8 Programmer: 1Set.
- 8.2 12V AC-DC adapter: 1 PCS.
- 8.3 USB cable: 1 PCS.
- 8.4 12-Pin Programming Cable: 8 PCS.

9 Characteristic;

- 9.1 Input Voltage: DC 9~12V.atleast 1A
- 9.2 USB1.2or above.
- 9.3 Output voltage: 3.3V or 5.0V±0.3V.
- 9.4 Max output Current: 300mA.
- 9.5 Working Temperature: -20C $^{\sim}$ 70C.
- 9.6 Internal Flash Erase : Endurance 100,000 Cycles;
- 11.7 Internal Flash Data Retention: More than 10years